## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Teruaki Kasai

Serial No.:

N/A

Filing Date:

Herewith

Title:

"APPARATUS AND METHOD FOR PICKING UP

SEMICONDUCTOR CHIP"

Docket No.:

36635

## **INFORMATION DISCLOSURE STATEMENT**

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In accordance with Rule 56, applicant is aware of the publications listed in the enclosed copy of Patent Office Form 1449. A copy of each of the publications is enclosed herewith.

Respectfully submitted, PEARNE & GORDON LLP

By:

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April 8, 2004

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	INF		CLOSURE CITATION		APPLICANT: Teruaki Kasai				
BY APPLICANT (USE SEVERAL SHEETS IF NE			_	ARY)	FILING DATE: Herewith		GROUP ART UNIT:		
			U.S. PA	ATENT DOCUME	NTS				
Examiner Initial		Document No.	Date	Name		Class	Sub	class	Filing Date If Appropriate
	Α	2004/0038498	2/2004	Ozono et al	•				
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## FOREIGN PATENT DOCUMENTS

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	Document No.	Date	Country	Class	Subclass	Translation
L	54-58356	5/1979	Japan			cited pg 1 of spec
M	05-335405	12/1993	Japan			English abstract
N	06-061347	3/1994	Japan			English abstract
P	11-274181	10/1999	Japan			English abstract
Q						

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)	
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\*Examiner: Initial if reference considered, regardless of whether citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.